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FORM PTO-892 (REV. 3-78)		U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		SERIAL NO. <b>103,192</b>	GROUP/ART UNIT <b>237</b>	ATTACHMENT TO PAPER NUMBER <b>5</b>
NOTICE OF REFERENCES CITED				APPLICANT(S) <b>Bergman</b>		

  

U.S. PATENT DOCUMENTS									
*	DOCUMENT NO.	DATE	NAME	CLASS	SUB-CLASS	FILING DATE IF APPROPRIATE			
A	4342082	7-27-82	Brown et al.	364	200				
B	4641275	2-3-87	Hatakeyama et al.	364	900				
C	4454578	6-12-84	Matsumoto et al.	364	200				
D	4395758	7-26-83	Helenius et al.	364	200				
E	4384324	5-17-83	Kim et al.	364	200				
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G	4136383	1-23-79	Takesue	364	200				
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Q									

  

OTHER REFERENCES (Including Author, Title, Date, Pertinent Pages, Etc.)	
R	Wu, W.H. et al., "Implementation of Optical Interconnections for VLSI", <u>IEEE Transactions on Electron Devices</u> , Vol. ED-34, No. 3, March, 1987, pp. 706 - 714
T	Bergman, L.A. et al. "Holographic Optical Interconnects For VLSI", <u>Optical Engineering</u> , Vol. 25, No. 10, October 1986, pp. 1109 - 1118.
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EXAMINER <b>Phil. K.</b>	DATE <b>5-9-89</b>
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\* A copy of this reference is not being furnished with this office action.  
(See Manual of Patent Examining Procedure, section 707.05 (a).)